Amendments to the Claims:

This listing of claims will replace all prior versions, and listings, of claims in the application.

1. (Currently Amended) A method of processing data which is communicated over a computer network, said method comprising:

pre-allocating portions of a memory to a first <u>network processing offload</u> processor and a second network processing offload processor;

receiving a first packet header data from a first network interface port,

processing said first packet header data in said first network

processing offload processor which executes a first network protocol

stack, executing a first socket software program with said first network

processing offload processor and transmitting a first application data

associated with said first packet header data to a host processing

system, wherein said first network processing offload processor

communicates with at least one host processor of said host processing

system;

receiving a second packet header data from said first network interface port, processing said second packet header data in said second network processing offload processor which executes a second network protocol stack, executing a second socket software program with said second network processing offload processor and transmitting a second application data associated with said second packet header data to said host processing system, wherein said second network processing offload processor communicates with at least one host processor of said host processing system;

processing of a third packet header data comprising executing a third socket software program with said first network processing offload processor, receiving a third application data from said host processing system and through execution of said first network processing offload processor preparing said third packet header data and causing said third application data and said third packet header data to be transmitted over said computer network through said first network interface port; processing of a fourth packet header data comprising executing a fourth socket software program with said second network processing offload processor, receiving a fourth application data from said host processing system and through execution of said second network processing offload processor preparing said fourth packet header data associated with said fourth application data and causing said fourth application data and said fourth packet header data to be transmitted over said computer network through said first network interface port; synchronizing access to said memory by said first and second network processing offload processors; and maintaining a communication channel between said first network processing

maintaining a communication channel between said first <u>network processing</u>

<u>offload processor and said second network processing offload</u>

processor through a message queue.

- 2. (Original) A method as in claim 1 wherein said first network protocol stack and said second network protocol stack are separate processing threads.
- 3. (Original) A method as in claim 2 wherein said separate processing threads each comprise separate operating system software processing logic.

4. (Original) A method as in claim 1 wherein said first network protocol stack

and said second network protocol stack use the same network protocols.

5. (Original) A method as in claim 4 wherein said same network protocols

comprise at least one of (a) an Internet Protocol (IP) and (b) a Transmission Control

Protocol (TCP).

6. (Original) A method as in claim 1 wherein said first group of network packets

are associated with a first network session between a host processing system and a

first digital processing system and said second group of network packets are

associated with a second network session between said host processing system and

a second digital processing system.

7-8. (Canceled)

9. (Original) A method as in claim 1 wherein said first network interface port

comprises an Ethernet interface.

10. (Original) A method as in claim 1 wherein said first group of network packets

are assigned to said first processor through a programmable hashing operation on

said first group of network packets and wherein said second group of network

packets are assigned to said second processor through said programmable hashing

operation.

11. (Currently Amended) A system for processing data which is communicated

over a computer network, said system comprising:

- a network interface port;
- a memory coupled to a bus;
- a host interface port;
- a host computing system communicably coupled to said host interface post;
- a first network processing offload processor coupled to said network interface port, said bus and said host interface port, said first network processing offload processor executing to execute a first network protocol stack to process a first group of network packets which are communicated through said network interface port, wherein said first network processing offload processor communicates with at least one host processor of said host processing system by executing a first socket software program;
- a second <u>network processing offload processor</u> coupled to said network interface port, said bus and said host interface port, said second <u>network processing offload</u> processor <u>executing to execute a second network protocol</u> stack to process a second group of network packets which are communicated through said network interface port, wherein said second <u>network processing offload processor</u> communicates with at least one host processor of said host processing system <u>by</u> <u>executing a second socket software program;</u>
- locking logic circuitry to synchronize access to said memory by said <u>network</u> processing offload processors;
- dispatch logic circuitry to assign a network packet to a specific one of said first and second network processing offload processors;
- a DMA engine and a control queue coupled to said network interface port and said host interface port through said bus to transfer packets to and from both said network interface and said host interface;

a message queue to maintain a communication channel between said first

network processing offload processor and said second network

processing offload processor;

wherein:

said first network processing offload processor to execute network

protocol stack instructions to process a received first packet
header data from said first network interface port; and
said second network processing offload processor to execute network
protocol stack instructions to process a received second packet
header data from said first network interface port.

- 12. (Original) A system as in claim 11 wherein said first network protocol stack and said second network protocol stack are separate processing threads.
- 13. (Original) A system as in claim 12 wherein said separate processing threads each comprise separate operating system processing logic.
- 14. (Original) A system as in claim 11 wherein said first network protocol stack and said second network protocol stack use the same network protocols.
- 15. (Original) A system as in claim 14 wherein said same network protocols comprise at least one of (a) an Internet Protocol (IP) and (b) a Transmission Control Protocol (TCP).
- 16. (Original) A system as in claim 11 wherein said first group of network packets are associated with a first network session between a host processing system and a

first digital processing system and said second group of network packets are associated with a second network session between said host processing system and a second digital processing system.

17-18. (Canceled)

19. (Original) A system as in claim 11 wherein said first network interface port

comprises an Ethernet interface.

20. (Original) A system as in claim 11 wherein said first group of network packets are assigned to said first processor through a programmable hashing operation on said first group of network packets and wherein said second group of network packets are assigned to said second processor through said programmable hashing

operation.

- 21. (Currently Amended) A system for processing data which is communicated over a computer network, said system comprising:
 - a network interface port;
 - a host interface port;
 - a host computing system communicably coupled to said host interface post;
 - a first network processing offload processor coupled to said network interface port, said first network processing offload processor executing to execute a first network protocol stack to process a first group of network packets which are communicated through said network interface port, wherein said first processor communicates with at least

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- one host processor of said host processing system by executing a first socket software program;
- a second <u>network processing offload processor coupled to said network</u>
 interface port, said second <u>network processing offload processor</u>
 executing to execute a second network protocol stack to process a
 second group of network packets which are communicated through
 said network interface port, wherein said second processor
 communicates with at least one host processor of said host processing
 system by executing a second socket software program;
- a first bus coupled to said first <u>network processing offload processor</u> and to said second <u>network processing offload processor</u> and to said network interface port;
- a first memory coupled to said first bus;
- a first memory controller coupled to said first bus and to said first memory, at least a portion of said first group of network packets and a portion of said second group of network packets being stored in said first memory.
- 22. (Original) A system as in claim 21 further comprising:
 - a host bus interface coupled to said first bus;
 - a second bus coupled to said host bus interface;
 - a second memory coupled to said second bus;
 - a second memory controller coupled to said second bus and to said second memory;
 - a host processor coupled to said second bus and to said second memory.

- 23. (Currently Amended) A system as in claim 21 wherein said first <u>network</u> <u>processing offload</u> processor, said second <u>network processing offload</u> processor, said first bus and said first memory controller are all fabricated on a single integrated circuit.
- 24. (Currently Amended) A system as in claim 22 wherein before said first network processing offload processor executes said first network protocol stack to process said first group of network packets, said portion of said first group is stored in said first memory through a first direct memory access (DMA) operation.
- 25. (Currently Amended) A system as in claim 24 wherein after said first <u>network</u> <u>processing offload</u> processor executes said first network protocol stack to process said first group, said portion of said first group is stored in said second memory through a second DMA operation.
- 26. (Original) A system as in claim 21 wherein said portion of said first group and said portion of said second group are stored in said first memory in pre-allocated portions of said first memory.
- 27. (Currently Amended) A system as in claim 21 further comprising:

 first dispatch logic coupled to said network interface port and to said first bus,

 said first dispatch logic assigning said first group to said first network

 processing offload processor through a programmable hashing

 operation on said first group.

- 28. (Currently Amended) A system as in claim 27 wherein said first dispatch logic assigns said second group to said second network processing offload processor through a programmable hashing operation.
- 29. (Currently Amended) A system as in claim 28 further comprising: second dispatch logic coupled to said first bus and to said host bus interface, said second dispatch logic assigning packets from said second bus to one of said first network processing offload processor or said second network processing offload processor.
- 30. (Original) A system as in claim 11 wherein said first processor and said second processor are general purpose, programmable processors.
- 31. (Currently Amended) A machine readable medium (MRM) containing executable program instructions which when executed by a processing system cause said processing system to perform a method of processing data which is communicated over a computer network, said method comprising:

pre-allocating portions of a memory to said a first network processing offload processor and said a second network processing offload processor;

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receiving first packet header data from a first network interface port, processing said first packet header data in a said first network processing offload processor which executes a first network protocol stack, and transmitting first application data associated with said first packet header data to a host processing system, wherein said first <u>network processing offload processor communicates with at least one</u> host processor of said host processing system by executing a first socket software program;

receiving second packet header data from said first network interface port,

processing a said second packet header data in a said second network

processing offload processor which executes a second network

protocol stack, and transmitting second application data associated

with said second packet header data to said host processing system,

wherein said second network processing offload processor

communicates with at least one host processor of said host processing

system by executing a second socket software program;

processing of a third packet header data comprising receiving third
application data from a host processing system, executing a third
socket software program with said first network processing offload
processor, and through execution of said first network processing
offload processor preparing said third packet header data and causing
said third application data and said third packet header data to be
transmitted over said computer network through said first network
interface port;

processing of a fourth packet header data comprising receiving fourth application data from said host processing system, executing a third socket software program with said first network processing offload processor, and through execution of said first network processing offload processor preparing said fourth packet header data associated with said fourth application data and causing said fourth application data and said fourth packet header data to be transmitted over said computer network through said first network interface port;

synchronizing access to said memory by said first and second processors;
and

maintaining a communication channel between said first processor and said

second processor through a message queue.

32. (Original) A MRM as in claim 31 wherein said first network protocol stack and

said second network protocol stack are separate processing threads.

33. (Original) A MRM as in claim 32 wherein said separate processing threads

each comprise separate operating system software processing logic.

34. (Original) A MRM as in claim 31 wherein said first network protocol stack and

said second network protocol stack use the same network protocols.

35. (Original) A MRM as in claim 34 wherein said same network protocols

comprise at least one of (a) an Internet Protocol (IP) and (b) a Transmission Control

Protocol (TCP).

36. (Original) A MRM as in claim 31 wherein said first group of network packets

are associated with a first network session between a host processing system and a

first digital processing system and said second group of network packets are

associated with a second network session between said host processing system and

a second digital processing system.

37-38. (Canceled)

39. (Original) A MRM as in claim 31 wherein said first network interface port

comprises an Ethernet interface.

- 40. (Currently Amended) A MRM as in claim 31 wherein said first group of network packets are assigned to said first network processing offload processor through a programmable hashing operation on said first group of network packets and wherein said second group of network packets are assigned to said second network processing offload processor through said programmable hashing operation.
- 41. (Original) A method as in claim 1 wherein said first network protocol stack and said second network protocol stack use different network protocols.
- 42. (Original) A system as in claim 11 wherein said first network protocol stack and said second network protocol stack use different network protocols.
- 43. (Original) A machine readable medium as in claim 31 wherein said first network protocol stack and said second network protocol stack use different network protocols.
- 44. (Currently Amended) A method as in claim 1 further comprising:

 processing a third group of network packets in said first network processing

 offload processor which executes said first network protocol stack, said
 third group of network packets being communicated through a second
 network interface port.
- 45. (Currently Amended) A system as in claim 11 further comprising: at least one further network interface port coupled to said first network processing offload processor and said second network processing offload processor.

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46. (Currently Amended) A system as in claim 11 wherein said first <u>network</u> processing offload processor and said second <u>network processing offload</u> processor are coupled to a further host processing system.